## **Faculty Profile**



Name of Teaching Staff	Mr.
Designation	Ass
Department	Elec
Date of Joining the Institution	12 <sup>th</sup>
Nature of Association (Regular/Contractual/Adjunct)	Reg
Area of Interest	Pow
Qualifications	UG
Total Experience in Years	Tea Mor
Papers Published in Journals	Inte
Papers Presented in Conferences	Inte
PhD Guided/ Projects Guided	PhI
<b>Books Published / IPR Details</b>	Boo
Professional Memberships	
Awards & Recognition, if any	

Mr. Pankaj Patidar						
Assistant Professor						
Electronics & Communication Engineering						
12 <sup>th</sup> January 2022						
Regular						
Power System, Electrical Machine, Control System						
	PG: M.Tech.(Power System) PhD: Pursuing					
rs 3	Industry: NIL		Research: NIL			
	NIL	National 1		1		
	NIL	National		NIL		
	NIL	Projects Guided		5		
Pat	ent: NIL	<b>Design Patent:</b> NIL		Copyright: NIL		
NIL						
1. Qualified GATE-2024 with GATE Score-380						
	r 1munio ectrica rs 3 Pat	r nmunication Engin ectrical Machine , PG: M.Tec System) Industry: NIL NIL NIL NIL Patent: NIL	r munication Engineering ectrical Machine , Control Syste PG: M.Tech.(Power System) rs 3 Industry: NIL NIL NIL NIL NIL National NIL National NIL Projects Guided Patent: NIL Design Pate NIL	r munication Engineering ectrical Machine , Control System $PG: M.Tech.(Power System) PhD: System) NIL Researce NIL NIL NIL NIL NIL Projects Guided Patent: NIL Design Patent: NIL $		